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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/JP03/09171 07/18/2003

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2002-211364 07/19/2002

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 13	INDEPENDENT CLAIMS 5
Verified and Acknowledged  Examiner's Signature <i>Koichi Kato</i>	Initials <i>JKS</i>				

## ADDRESS

21395

## TITLE

Burst signal extinction ratio control circuit, integrated circuit thereof, burst signal extinction ratio control method, computer program, and laser diode drive circuit

FILING FEE RECEIVED 1300	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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